

Tyan i7501 Pro (S2721-533) lock-up (e1000?)

Source: <http://linux.derkeiler.com/Mailing-Lists/Kernel/2003-09/7518.html>

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Date: Mon, 29 Sep 2003 18:37:51 -0400 (EDT)
To: Linux Kernel Mail List <linux-kernel@vger.kernel.org>

I have a pair of these critters (one single, one dual Xeon) that constantly lock up with any volume of traffic hits them. It doesn't appear to matter which kernel version (2.4.20, 2.6.0 bk+) --- they all eventually lockup.

I've tried with "acpi=off", "pci=noacpi", etc. In gig mode, it'll lockup within seconds (with 25Mbps thrown at it.) In 100M/Full, it'll last a few minutes. And at 100M/half, it'll last hours. Of course, they're being ultra helpful by simply freezing without a single blip of why.

Anyone else seeing similar problems with the i7501 pro?

```
[root@xxx root]# lsmod
Module Size Used by
autofs 18560 0
e1000 92032 0
e100 64900 0
[root@xxx root]# grep e1000 /etc/modprobe.conf
alias eth1 e1000
alias eth2 e1000
options e1000 FlowControl=0,0 RxDescriptors=4096,4096 TxDescriptors=1024,1024
```

Linux version 2.6.0-SMP-test6+BK@1.1347 (root@xxx) (gcc version 3.2.2 20030222 (Red Hat Linux 3.2.2-5)) #1 SMP BK(03/09/29-16:22:08-04:00) Mon Sep 29 16:49:37 EDT 2003

Video mode to be used for restore is f00

BIOS-provided physical RAM map:

```
BIOS-e820: 0000000000000000 - 000000000009fc00 (usable)
BIOS-e820: 000000000009fc00 - 00000000000a0000 (reserved)
BIOS-e820: 00000000000e0000 - 0000000000100000 (reserved)
BIOS-e820: 0000000000100000 - 0000000007fff000 (usable)
BIOS-e820: 0000000007fff000 - 000000007fff000 (ACPI data)
BIOS-e820: 000000007fff000 - 0000000080000000 (ACPI NVS)
BIOS-e820: 00000000fec00000 - 00000000fed00000 (reserved)
BIOS-e820: 00000000fee00000 - 00000000fee01000 (reserved)
BIOS-e820: 00000000ffb80000 - 0000000100000000 (reserved)
```

1151MB HIGHMEM available.

896MB LOWMEM available.

found SMP MP-table at 000ff780

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hm, page 000ff000 reserved twice.
hm, page 00100000 reserved twice.
hm, page 000f9000 reserved twice.
hm, page 000fa000 reserved twice.
On node 0 totalpages: 524272
DMA zone: 4096 pages, LIFO batch:1
Normal zone: 225280 pages, LIFO batch:16
HighMem zone: 294896 pages, LIFO batch:16
DMI 2.3 present.
ACPI: RSDP (v000 ACPIAM) @ 0x000f5020
ACPI: RSDT (v001 A M I OEMRSDT 0x04000314 MSFT 0x00000097) @ 0x7fff0000
ACPI: FADT (v002 A M I OEMFACP 0x04000314 MSFT 0x00000097) @ 0x7fff0200
ACPI: MADT (v001 A M I OEMAPIC 0x04000314 MSFT 0x00000097) @ 0x7fff0300
ACPI: OEMB (v001 A M I OEMBIOS 0x04000314 MSFT 0x00000097) @ 0x7ffff040
ACPI: DSDT (v001 0ABBP 0ABBP003 0x00000003 INTL 0x02002026) @ 0x00000000
ACPI: Local APIC address 0xfee00000
ACPI: LAPIC (acpi_id[0x01] lapic_id[0x00] enabled)
Processor #0 15:2 APIC version 20
ACPI: LAPIC (acpi_id[0x02] lapic_id[0x06] enabled)
Processor #6 15:2 APIC version 20
ACPI: LAPIC (acpi_id[0x03] lapic_id[0x82] disabled)
Processor #130 invalid (max 16)
ACPI: LAPIC (acpi_id[0x04] lapic_id[0x83] disabled)
Processor #131 invalid (max 16)
Using ACPI for processor (LAPIC) configuration information
Intel MultiProcessor Specification v1.4
Virtual Wire compatibility mode.
OEM ID: INTEL Product ID: KingsCanyonC APIC at: 0xFEE00000
I/O APIC #7 Version 32 at 0xFEC00000.
I/O APIC #8 Version 32 at 0xFEC80000.
I/O APIC #9 Version 32 at 0xFEC80400.
I/O APIC #10 Version 32 at 0xFEC81000.
I/O APIC #11 Version 32 at 0xFEC81400.
Enabling APIC mode: Flat. Using 5 I/O APICs
Processors: 2
Building zonelist for node : 0
Kernel command line: root=/dev/hda3 pci=noacpi
Initializing CPU#0
PID hash table entries: 4096 (order 12: 32768 bytes)
Detected 2791.341 MHz processor.
Console: colour VGA+ 80x25
Memory: 2072432k/2097088k available (1586k kernel code, 23524k reserved, 654k data, 136k init, 1179584k highmem)
Calibrating delay loop... 5505.02 BogoMIPS
Dentry cache hash table entries: 262144 (order: 8, 1048576 bytes)
Inode-cache hash table entries: 131072 (order: 7, 524288 bytes)
Mount-cache hash table entries: 512 (order: 0, 4096 bytes)
CPU: After generic identify, caps: bfebfbff 00000000 00000000 00000000
CPU: After vendor identify, caps: bfebfbff 00000000 00000000 00000000
CPU: Trace cache: 12K uops, L1 D cache: 8K
CPU: L2 cache: 512K

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```
CPU: Hyper-Threading is disabled
CPU: After all inits, caps: bfebfbff 00000000 00000000 00000080
Intel machine check architecture supported.
Intel machine check reporting enabled on CPU#0.
CPU#0: Intel P4/Xeon Extended MCE MSR(12) available
CPU#0: Thermal monitoring enabled
Enabling fast FPU save and restore... done.
Enabling unmasked SIMD FPU exception support... done.
Checking 'hlt' instruction... OK.
POSIX conformance testing by UNIFIX
CPU0: Intel(R) Xeon(TM) CPU 2.80GHz stepping 07
per-CPU timeslice cutoff: 1462.54 usecs.
task migration cache decay timeout: 2 msecs.
enabled ExtINT on CPU#0
ESR value before enabling vector: 00000000
ESR value after enabling vector: 00000000
Booting processor 1/6 eip 2000
Initializing CPU#1
masked ExtINT on CPU#1
ESR value before enabling vector: 00000000
ESR value after enabling vector: 00000000
Calibrating delay loop... 5570.56 BogoMIPS
CPU: After generic identify, caps: bfebfbff 00000000 00000000 00000000
CPU: After vendor identify, caps: bfebfbff 00000000 00000000 00000000
CPU: Trace cache: 12K uops, L1 D cache: 8K
CPU: L2 cache: 512K
CPU: Hyper-Threading is disabled
CPU: After all inits, caps: bfebfbff 00000000 00000000 00000080
Intel machine check architecture supported.
Intel machine check reporting enabled on CPU#1.
CPU#1: Intel P4/Xeon Extended MCE MSR(12) available
CPU#1: Thermal monitoring enabled
CPU1: Intel(R) Xeon(TM) CPU 2.80GHz stepping 07
Total of 2 processors activated (11075.58 BogoMIPS).
ENABLING IO-APIC IRQs
Setting 7 in the phys_id_present_map
...changing IO-APIC physical APIC ID to 7 ... ok.
Setting 8 in the phys_id_present_map
...changing IO-APIC physical APIC ID to 8 ... ok.
Setting 9 in the phys_id_present_map
...changing IO-APIC physical APIC ID to 9 ... ok.
Setting 10 in the phys_id_present_map
...changing IO-APIC physical APIC ID to 10 ... ok.
Setting 11 in the phys_id_present_map
...changing IO-APIC physical APIC ID to 11 ... ok.
init IO-APIC IRQs
IO-APIC (apicid-pin) 7-0, 7-9, 7-11, 7-15, 7-16, 7-20, 7-21, 7-22, 7-23, 8-0, 8-1, 8-2, 8-3, 8-4, 8-5,
8-6, 8-7, 8-8, 8-9, 8-10, 8-11, 8-12, 8-13, 8-14, 8-15, 8-16, 8-17, 8-18, 8-19, 8-20, 8-21, 8-22, 8-23,
9-2, 9-3, 9-4, 9-5, 9-6, 9-7, 9-8, 9-9, 9-10, 9-11, 9-12, 9-13, 9-14, 9-15, 9-16, 9-17, 9-18, 9-19,
9-20, 9-21, 9-22, 9-23, 10-0, 10-1, 10-2, 10-3, 10-4, 10-5, 10-6, 10-7, 10-8, 10-9, 10-10, 10-11,
10-12, 10-13, 10-14, 10-15, 10-16, 10-17, 10-18, 10-19, 10-20, 10-21, 10-22, 10-23, 11-0, 11-1, 11-2,
```

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11-3, 11-4, 11-5, 11-6, 11-7, 11-8, 11-9, 11-10, 11-11, 11-12, 11-13, 11-14, 11-15, 11-16, 11-17, 11-18, 11-19, 11-20, 11-21, 11-22, 11-23 not connected.

..TIMER: vector=0x31 pin1=2 pin2=0

number of MP IRQ sources: 19.

number of IO-APIC #7 registers: 24.

number of IO-APIC #8 registers: 24.

number of IO-APIC #9 registers: 24.

number of IO-APIC #10 registers: 24.

number of IO-APIC #11 registers: 24.

testing the IO APIC.....

IO APIC #7.....

.... register #00: 07000000

..... : physical APIC id: 07

..... : Delivery Type: 0

..... : LTS : 0

.... register #01: 00178020

..... : max redirection entries: 0017

..... : PRQ implemented: 1

..... : IO APIC version: 0020

.... register #02: 00000000

..... : arbitration: 00

.... register #03: 00000001

..... : Boot DT : 1

.... IRQ redirection table:

NR Log Phy Mask Trig IRR Pol Stat Dest Deli Vect:

00 000 00 1 0 0 0 0 0 2 16

01 001 01 0 0 0 0 0 1 1 39

02 001 01 0 0 0 0 0 1 1 31

03 001 01 0 0 0 0 0 1 1 41

04 001 01 0 0 0 0 0 1 1 49

05 001 01 0 0 0 0 0 1 1 51

06 001 01 0 0 0 0 0 1 1 59

07 001 01 0 0 0 0 0 1 1 61

08 001 01 0 0 0 0 0 1 1 69

09 000 00 1 0 0 0 0 0 0 00

0a 001 01 0 0 0 0 0 1 1 71

0b 000 00 1 0 0 0 0 0 0 00

0c 001 01 0 0 0 0 0 1 1 79

0d 001 01 0 0 0 0 0 1 1 81

0e 001 01 0 0 0 0 0 1 1 89

0f 000 00 1 0 0 0 0 0 0 00

10 000 00 1 0 0 0 0 0 0 00

11 001 01 1 1 0 1 0 1 1 91

12 001 01 1 1 0 1 0 1 1 99

13 001 01 1 1 0 1 0 1 1 A1

14 000 00 1 0 0 0 0 0 0 00

15 006 06 1 0 0 0 0 0 2 40

16 012 02 1 0 0 0 0 0 2 24

17 052 02 1 0 0 0 0 0 2 40

IO APIC #8.....

.... register #00: 08000000

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```
..... : physical APIC id: 08
..... : Delivery Type: 0
..... : LTS : 0
.... register #01: 00178020
..... : max redirection entries: 0017
..... : PRQ implemented: 1
..... : IO APIC version: 0020
.... register #02: 08000000
..... : arbitration: 08
.... register #03: 00000001
..... : Boot DT : 1
.... IRQ redirection table:
NR Log Phy Mask Trig IRR Pol Stat Dest Deli Vect:
00 000 00 1 0 0 0 0 0 0 00
01 000 00 1 0 0 0 0 0 0 00
02 000 00 1 0 0 0 0 0 0 00
03 000 00 1 0 0 0 0 0 0 00
04 000 00 1 0 0 0 0 0 0 00
05 000 00 1 0 0 0 0 0 0 00
06 000 00 1 0 0 0 0 0 0 00
07 000 00 1 0 0 0 0 0 0 00
08 000 00 1 0 0 0 0 0 0 00
09 000 00 1 0 0 0 0 0 0 00
0a 000 00 1 0 0 0 0 0 0 00
0b 000 00 1 0 0 0 0 0 0 00
0c 000 00 1 0 0 0 0 0 0 00
0d 000 00 1 0 0 0 0 0 0 00
0e 000 00 1 0 0 0 0 0 0 00
0f 000 00 1 0 0 0 0 0 0 00
10 000 00 1 0 0 0 0 0 0 00
11 000 00 1 0 0 0 0 0 0 00
12 000 00 1 0 0 0 0 0 0 00
13 000 00 1 0 0 0 0 0 0 00
14 000 00 1 0 0 0 0 0 0 00
15 000 00 1 0 0 0 0 0 0 00
16 000 00 1 0 0 0 0 0 0 00
17 000 00 1 0 0 0 0 0 0 00
IO APIC #9.....
.... register #00: 09000000
..... : physical APIC id: 09
..... : Delivery Type: 0
..... : LTS : 0
.... register #01: 00178020
..... : max redirection entries: 0017
..... : PRQ implemented: 1
..... : IO APIC version: 0020
.... register #02: 09000000
..... : arbitration: 09
.... register #03: 00000001
..... : Boot DT : 1
.... IRQ redirection table:
```

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NR Log Phy Mask Trig IRR Pol Stat Dest Deli Vect:

00 001 01 1 1 0 1 0 1 1 A9
01 001 01 1 1 0 1 0 1 1 B1
02 000 00 1 0 0 0 0 0 0 00
03 000 00 1 0 0 0 0 0 0 00
04 000 00 1 0 0 0 0 0 0 00
05 000 00 1 0 0 0 0 0 0 00
06 000 00 1 0 0 0 0 0 0 00
07 000 00 1 0 0 0 0 0 0 00
08 000 00 1 0 0 0 0 0 0 00
09 000 00 1 0 0 0 0 0 0 00
0a 000 00 1 0 0 0 0 0 0 00
0b 000 00 1 0 0 0 0 0 0 00
0c 000 00 1 0 0 0 0 0 0 00
0d 000 00 1 0 0 0 0 0 0 00
0e 000 00 1 0 0 0 0 0 0 00
0f 000 00 1 0 0 0 0 0 0 00
10 000 00 1 0 0 0 0 0 0 00
11 000 00 1 0 0 0 0 0 0 00
12 000 00 1 0 0 0 0 0 0 00
13 000 00 1 0 0 0 0 0 0 00
14 000 00 1 0 0 0 0 0 0 00
15 000 00 1 0 0 0 0 0 0 00
16 000 00 1 0 0 0 0 0 0 00
17 000 00 1 0 0 0 0 0 0 00

IO APIC #10.....

.... register #00: 0A000000
..... : physical APIC id: 0A
..... : Delivery Type: 0
..... : LTS : 0
.... register #01: 00178020
..... : max redirection entries: 0017
..... : PRQ implemented: 1
..... : IO APIC version: 0020
.... register #02: 0A000000
..... : arbitration: 0A
.... register #03: 00000001
..... : Boot DT : 1

.... IRQ redirection table:

NR Log Phy Mask Trig IRR Pol Stat Dest Deli Vect:

00 000 00 1 0 0 0 0 0 0 00
01 000 00 1 0 0 0 0 0 0 00
02 000 00 1 0 0 0 0 0 0 00
03 000 00 1 0 0 0 0 0 0 00
04 000 00 1 0 0 0 0 0 0 00
05 000 00 1 0 0 0 0 0 0 00
06 000 00 1 0 0 0 0 0 0 00
07 000 00 1 0 0 0 0 0 0 00
08 000 00 1 0 0 0 0 0 0 00
09 000 00 1 0 0 0 0 0 0 00
0a 000 00 1 0 0 0 0 0 0 00

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0b 000 00 1 0 0 0 0 0 0 0
0c 000 00 1 0 0 0 0 0 0 0
0d 000 00 1 0 0 0 0 0 0 0
0e 000 00 1 0 0 0 0 0 0 0
0f 000 00 1 0 0 0 0 0 0 0
10 000 00 1 0 0 0 0 0 0 0
11 000 00 1 0 0 0 0 0 0 0
12 000 00 1 0 0 0 0 0 0 0
13 000 00 1 0 0 0 0 0 0 0
14 000 00 1 0 0 0 0 0 0 0
15 000 00 1 0 0 0 0 0 0 0
16 000 00 1 0 0 0 0 0 0 0
17 000 00 1 0 0 0 0 0 0 0

IO APIC #11.....

.... register #00: 0B000000
..... : physical APIC id: 0B
..... : Delivery Type: 0
..... : LTS : 0
.... register #01: 00178020
..... : max redirection entries: 0017
..... : PRQ implemented: 1
..... : IO APIC version: 0020
.... register #02: 0B000000
..... : arbitration: 0B
.... register #03: 00000001
..... : Boot DT : 1

.... IRQ redirection table:

NR Log Phy Mask Trig IRR Pol Stat Dest Deli Vect:

00 000 00 1 0 0 0 0 0 0 0
01 000 00 1 0 0 0 0 0 0 0
02 000 00 1 0 0 0 0 0 0 0
03 000 00 1 0 0 0 0 0 0 0
04 000 00 1 0 0 0 0 0 0 0
05 000 00 1 0 0 0 0 0 0 0
06 000 00 1 0 0 0 0 0 0 0
07 000 00 1 0 0 0 0 0 0 0
08 000 00 1 0 0 0 0 0 0 0
09 000 00 1 0 0 0 0 0 0 0
0a 000 00 1 0 0 0 0 0 0 0
0b 000 00 1 0 0 0 0 0 0 0
0c 000 00 1 0 0 0 0 0 0 0
0d 000 00 1 0 0 0 0 0 0 0
0e 000 00 1 0 0 0 0 0 0 0
0f 000 00 1 0 0 0 0 0 0 0
10 000 00 1 0 0 0 0 0 0 0
11 000 00 1 0 0 0 0 0 0 0
12 000 00 1 0 0 0 0 0 0 0
13 000 00 1 0 0 0 0 0 0 0
14 000 00 1 0 0 0 0 0 0 0
15 000 00 1 0 0 0 0 0 0 0
16 000 00 1 0 0 0 0 0 0 0

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17 000 00 1 0 0 0 0 0 0 0

IRQ to pin mappings:

IRQ0 -> 0:2

IRQ1 -> 0:1

IRQ3 -> 0:3

IRQ4 -> 0:4

IRQ5 -> 0:5

IRQ6 -> 0:6

IRQ7 -> 0:7

IRQ8 -> 0:8

IRQ10 -> 0:10

IRQ12 -> 0:12

IRQ13 -> 0:13

IRQ14 -> 0:14

IRQ17 -> 0:17

IRQ18 -> 0:18

IRQ19 -> 0:19

IRQ48 -> 2:0

IRQ49 -> 2:1

..... done.

Using local APIC timer interrupts.

calibrating APIC timer ...

..... CPU clock speed is 2790.0390 MHz.

..... host bus clock speed is 132.0875 MHz.

checking TSC synchronization across 2 CPUs: passed.

Starting migration thread for cpu 0

Bringing up 1

CPU 1 IS NOW UP!

Starting migration thread for cpu 1

CPUS done 4

NET: Registered protocol family 16

PCI: PCI BIOS revision 2.10 entry at 0xf0031, last bus=7

PCI: Using configuration type 1

mtrr: v2.0 (20020519)

ACPI: Subsystem revision 20030813

ACPI: Interpreter enabled

ACPI: Using PIC for interrupt routing

ACPI: PCI Root Bridge [PCI0] (00:00)

PCI: Probing PCI hardware (bus 00)

Transparent bridge - 0000:00:1e.0

ACPI: PCI Interrupt Routing Table [_SB_.PCI0._PRT]

ACPI: PCI Interrupt Routing Table [_SB_.PCI0.P0P1._PRT]

ACPI: PCI Interrupt Routing Table [_SB_.PCI0.P0P5.P5P6._PRT]

ACPI: PCI Interrupt Routing Table [_SB_.PCI0.P0P5.P5P7._PRT]

ACPI: PCI Interrupt Routing Table [_SB_.PCI0.P0P2.P2P3._PRT]

ACPI: PCI Interrupt Routing Table [_SB_.PCI0.P0P2.P2P4._PRT]

ACPI: PCI Interrupt Link [LNKA] (IRQs 3 4 5 6 7 9 10 11 12 14 *15)

ACPI: PCI Interrupt Link [LNKB] (IRQs 3 4 5 6 7 9 10 11 12 14 15, disabled)

ACPI: PCI Interrupt Link [LNKC] (IRQs 3 4 5 6 7 9 10 *11 12 14 15)

ACPI: PCI Interrupt Link [LNKD] (IRQs 3 4 5 6 7 *9 10 11 12 14 15)

ACPI: PCI Interrupt Link [LNKE] (IRQs 3 4 5 6 7 9 10 11 12 14 15, disabled)

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ACPI: PCI Interrupt Link [LNKF] (IRQs 3 4 5 6 7 9 10 11 12 14 15, disabled)
ACPI: PCI Interrupt Link [LNKG] (IRQs 3 4 5 6 7 9 10 11 12 14 15, disabled)
ACPI: PCI Interrupt Link [LNKH] (IRQs 3 4 5 6 7 9 10 11 12 14 15, disabled)
PCI: Probing PCI hardware
PCI: Using IRQ router PIIX [8086/2480] at 0000:00:1f.0
PCI->APIC IRQ transform: (B0,I31,P0) -> 18
PCI->APIC IRQ transform: (B0,I31,P1) -> 17
PCI->APIC IRQ transform: (B7,I1,P0) -> 48
PCI->APIC IRQ transform: (B7,I1,P1) -> 49
PCI->APIC IRQ transform: (B1,I1,P0) -> 19
PCI->APIC IRQ transform: (B1,I2,P0) -> 18
Machine check exception polling timer started.
Starting balanced_irq
highmem bounce pool size: 64 pages
VFS: Disk quotas dquot_6.5.1
Initializing Cryptographic API
pty: 2048 Unix98 ptys configured
Serial: 8250/16550 driver \$Revision: 1.90 \$ IRQ sharing disabled
ttyS0 at I/O 0x3f8 (irq = 4) is a 16550A
ttyS1 at I/O 0x2f8 (irq = 3) is a 16550A
Uniform Multi-Platform E-IDE driver Revision: 7.00alpha2
ide: Assuming 33MHz system bus speed for PIO modes; override with idebus=xx
ICH3: IDE controller at PCI slot 0000:00:1f.1
PCI: Enabling device 0000:00:1f.1 (0005 -> 0007)
ICH3: chipset revision 2
ICH3: not 100% native mode: will probe irqs later
 ide0: BM-DMA at 0xffa0-0xffa7, BIOS settings: hda:DMA, hdb:pio
hda: ST340014A, ATA DISK drive
Using anticipatory io scheduler
ide0 at 0x1f0-0x1f7,0x3f6 on irq 14
hda: max request size: 1024KiB
hda: 78165360 sectors (40020 MB) w/2048KiB Cache, CHS=16383/255/63, UDMA(100)
hda: TCQ not supported
 hda: hda1 hda2 hda3
mice: PS/2 mouse device common for all mice
serio: i8042 AUX port at 0x60,0x64 irq 12
input: AT Translated Set 2 keyboard on isa0060/serio0
serio: i8042 KBD port at 0x60,0x64 irq 1
NET: Registered protocol family 2
IP: routing cache hash table of 16384 buckets, 128Kbytes
TCP: Hash tables configured (established 524288 bind 65536)
NET: Registered protocol family 1
NET: Registered protocol family 17
EXT3-fs: INFO: recovery required on readonly filesystem.
EXT3-fs: write access will be enabled during recovery.
kjournald starting. Commit interval 5 seconds
EXT3-fs: recovery complete.
EXT3-fs: mounted filesystem with ordered data mode.
VFS: Mounted root (ext3 filesystem) readonly.
Freeing unused kernel memory: 136k freed
EXT3 FS on hda3, internal journal

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Adding 2040244k swap on /dev/hda2. Priority:-1 extents:1
kjournald starting. Commit interval 5 seconds
EXT3 FS on hda1, internal journal
EXT3-fs: mounted filesystem with ordered data mode.

(lspci -n)

00:00.0 Class 0600: 8086:254c (rev 01)
00:02.0 Class 0604: 8086:2543 (rev 01)
00:03.0 Class 0604: 8086:2545 (rev 01)
00:1e.0 Class 0604: 8086:244e (rev 42)
00:1f.0 Class 0601: 8086:2480 (rev 02)
00:1f.1 Class 0101: 8086:248b (rev 02)
00:1f.3 Class 0c05: 8086:2483 (rev 02)
01:01.0 Class 0200: 8086:1229 (rev 10)
01:02.0 Class 0300: 1002:4752 (rev 27)
02:1c.0 Class 0800: 8086:1461 (rev 04)
02:1d.0 Class 0604: 8086:1460 (rev 04)
02:1e.0 Class 0800: 8086:1461 (rev 04)
02:1f.0 Class 0604: 8086:1460 (rev 04)
05:1c.0 Class 0800: 8086:1461 (rev 04)
05:1d.0 Class 0604: 8086:1460 (rev 04)
05:1e.0 Class 0800: 8086:1461 (rev 04)
05:1f.0 Class 0604: 8086:1460 (rev 04)
07:01.0 Class 0200: 8086:1010 (rev 01)
07:01.1 Class 0200: 8086:1010 (rev 01)

(lspci -vv -xxx)

00:00.0 Host bridge: Intel Corp.: Unknown device 254c (rev 01)
Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+
FastB2B-
Status: Cap+ 66Mhz- UDF- FastB2B+ ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort-
>SERR- <PERR-
Latency: 0
Capabilities: [40] #09 [1105]
00: 86 80 4c 25 06 01 90 00 01 00 00 06 00 00 00 00
10: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30: 00 00 00 00 40 00 00 00 00 00 00 00 00 00 00 00
40: 09 00 05 11 00 00 00 00 00 00 00 00 00 00 00 00
50: 04 20 0d 00 00 00 00 00 00 10 11 11 00 00 33 33
60: 00 00 08 10 18 20 20 20 00 00 00 00 00 00 00 00
70: 00 33 33 00 00 00 00 00 04 00 01 28 79 02 67 20
80: 62 06 71 00 00 00 00 00 98 10 d2 89 00 00 00
90: 00 00 00 00 00 00 00 00 55 05 55 05 03 1a 38 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 44 c0 50 11 00 80 ff 03 00 00 00 00 00 00 00 00
d0: 02 28 00 0e 03 00 00 33 80 09 31 b5 00 00 01 01
e0: 1d 1d 00 00 00 00 00 00 54 46 00 00 00 00 00 00
f0: 00 00 00 00 74 00 30 40 40 0f 00 00 00 00 00 00

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00:02.0 PCI bridge: Intel Corp. e7500 [Plumas] HI_B Virtual PCI Bridge (F0) (rev 01) (prog-if 00 [Normal decode])

Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap- 66Mhz+ UDF- FastB2B+ ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 64

Bus: primary=00, secondary=05, subordinate=07, sec-latency=0

I/O behind bridge: 0000e000-0000efff

Memory behind bridge: fe800000-feafffff

Prefetchable memory behind bridge: ff700000-ff9fffff

BridgeCtl: Parity- SERR+ NoISA+ VGA- MAbort- >Reset- FastB2B-

00: 86 80 43 25 07 01 a0 00 01 00 04 06 00 40 01 00
10: 00 00 00 00 00 00 00 00 00 05 07 00 e0 e0 a0 22
20: 80 fe a0 fe 70 ff 90 ff 00 00 00 00 00 00 00 00
30: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 06 00
40: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 3e 00 00 0e 54 46 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00:03.0 PCI bridge: Intel Corp. e7500 [Plumas] HI_C Virtual PCI Bridge (F0) (rev 01) (prog-if 00 [Normal decode])

Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap- 66Mhz+ UDF- FastB2B+ ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 64

Bus: primary=00, secondary=02, subordinate=04, sec-latency=0

I/O behind bridge: 0000c000-0000dfff

Memory behind bridge: fe500000-fe7fffff

Prefetchable memory behind bridge: ff400000-ff6fffff

BridgeCtl: Parity- SERR+ NoISA+ VGA- MAbort- >Reset- FastB2B-

00: 86 80 45 25 07 01 a0 00 01 00 04 06 00 40 01 00
10: 00 00 00 00 00 00 00 00 00 02 04 00 c0 d0 a0 22
20: 50 fe 70 fe 40 ff 60 ff 00 00 00 00 00 00 00 00
30: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 06 00
40: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 3e 00 00 0e 56 44 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

00:1e.0 PCI bridge: Intel Corp. 82801BA/CA/DB PCI Bridge (rev 42) (prog-if 00 [Normal decode])

Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap- 66Mhz- UDF- FastB2B+ ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 0

Bus: primary=00, secondary=01, subordinate=01, sec-latency=32

I/O behind bridge: 0000b000-0000bfff

Memory behind bridge: fc400000-fe4fffff

Prefetchable memory behind bridge: ff300000-ff3fffff

BridgeCtl: Parity- SERR+ NoISA- VGA+ MAbort- >Reset- FastB2B-

00: 86 80 4e 24 07 01 80 00 42 00 04 06 00 00 01 00
10: 00 00 00 00 00 00 00 00 00 01 01 20 b0 b0 80 22
20: 40 fc 40 fe 30 ff 30 ff 00 00 00 00 00 00 00 00
30: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 0a 00
40: 02 28 20 20 00 01 00 00 00 00 00 00 00 00 00 00
50: 02 14 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 40 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 88 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 10 00 08 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 01 00 02 00 00 00 c0 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 47 0f 00 00 00 00 6b 2c

00:1f.0 ISA bridge: Intel Corp. 82801CA ISA Bridge (LPC) (rev 02)

Control: I/O+ Mem+ BusMaster+ SpecCycle+ MemWINV- VGASnoop- ParErr- Stepping- SERR- FastB2B-

Status: Cap- 66Mhz- UDF- FastB2B+ ParErr- DEVSEL=medium >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 0

00: 86 80 80 24 0f 00 80 02 02 00 01 06 00 00 80 00
10: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40: 01 04 00 00 10 00 00 00 00 00 00 00 00 00 00 00
50: 00 00 00 00 00 00 00 00 01 05 00 00 10 00 00 00
60: 0f 80 0b 09 d1 00 00 00 80 80 80 80 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: ff fc 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

a0: 00 02 00 00 00 00 00 00 0d 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 86 21 00 00 02 05 00 00 04 00 00 00 00 00 00 00
e0: 10 00 00 c0 00 00 0f 14 33 22 11 00 00 00 67 45
f0: 00 00 60 87 00 00 00 00 47 0f 0e 00 00 00 81 00

00:1f.1 IDE interface: Intel Corp. 82801CA IDE U100 (rev 02) (prog-if 8a [Master SecP PriP])

Subsystem: Intel Corp.: Unknown device 2480

Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR-
FastB2B-

Status: Cap- 66Mhz- UDF- FastB2B+ ParErr- DEVSEL=medium >TAbort- <TAbort- <MAbort-
>SERR- <PERR-

Latency: 0

Interrupt: pin A routed to IRQ 18

Region 0: I/O ports at <unassigned>

Region 1: I/O ports at <unassigned>

Region 2: I/O ports at <unassigned>

Region 3: I/O ports at <unassigned>

Region 4: I/O ports at ffa0 [size=16]

Region 5: Memory at 80000000 (32-bit, non-prefetchable) [size=1K]

00: 86 80 8b 24 07 00 80 02 02 8a 01 01 00 00 00 00
10: 01 00 00 00 01 00 00 00 01 00 00 00 01 00 00 00
20: a1 ff 00 00 00 00 80 00 00 00 00 86 80 80 24
30: 00 00 00 00 00 00 00 00 00 00 00 00 01 00 00
40: 07 e3 00 00 00 00 00 00 01 00 01 00 00 00 00
50: 00 00 00 00 31 14 00 00 00 00 00 00 00 00 00
60: 08 00 00 00 00 00 00 00 08 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 47 0f 00 00 00 00 00

00:1f.3 SMBus: Intel Corp. 82801CA/CAM SMBus (rev 02)

Subsystem: Intel Corp.: Unknown device 2480

Control: I/O+ Mem- BusMaster- SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR-
FastB2B-

Status: Cap- 66Mhz- UDF- FastB2B+ ParErr- DEVSEL=medium >TAbort- <TAbort- <MAbort-
>SERR- <PERR-

Interrupt: pin B routed to IRQ 17

Region 4: I/O ports at 0540 [size=32]

00: 86 80 83 24 01 00 80 02 02 00 05 0c 00 00 00 00
10: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20: 41 05 00 00 00 00 00 00 00 00 00 86 80 80 24
30: 00 00 00 00 00 00 00 00 00 00 00 00 02 00 00
40: 01 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

50: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 47 0f 00 00 00 00 00 00

01:01.0 Ethernet controller: Intel Corp. 82557/8/9 [Ethernet Pro 100] (rev 10)

Subsystem: Intel Corp. EtherExpress PRO/100 S Server Adapter

Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV+ VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap+ 66Mhz- UDF- FastB2B+ ParErr- DEVSEL=medium >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 64 (2000ns min, 14000ns max), cache line size 10

Interrupt: pin A routed to IRQ 19

Region 0: Memory at fe4fe000 (32-bit, non-prefetchable) [size=4K]

Region 1: I/O ports at bc00 [size=64]

Region 2: Memory at fe4a0000 (32-bit, non-prefetchable) [size=128K]

Capabilities: [dc] Power Management version 2

Flags: PMEClk- DSI+ D1+ D2+ AuxCurrent=0mA PME(D0+,D1+,D2+,D3hot+,D3cold+)

Status: D0 PME-Enable- DSel=0 DScale=2 PME-

00: 86 80 29 12 17 01 90 02 10 00 00 02 10 40 00 00
10: 00 e0 4f fe 01 bc 00 00 00 00 4a fe 00 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 00 86 80 40 10
30: 00 00 00 00 dc 00 00 00 00 00 00 00 09 01 08 38
40: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 01 00 22 fe
e0: 00 40 00 4b 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

01:02.0 VGA compatible controller: ATI Technologies Inc Rage XL (rev 27) (prog-if 00 [VGA])

Subsystem: ATI Technologies Inc Rage XL

Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping+ SERR- FastB2B-

Status: Cap+ 66Mhz- UDF- FastB2B+ ParErr- DEVSEL=medium >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 64 (2000ns min), cache line size 10

Interrupt: pin A routed to IRQ 18

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

Region 0: Memory at fd000000 (32-bit, non-prefetchable) [size=16M]

Region 1: I/O ports at b800 [size=256]

Region 2: Memory at fe4ff000 (32-bit, non-prefetchable) [size=4K]

Expansion ROM at fe4c0000 [disabled] [size=128K]

Capabilities: [5c] Power Management version 2

Flags: PMEClk- DSI- D1+ D2+ AuxCurrent=0mA PME(D0-,D1-,D2-,D3hot-,D3cold-)

Status: D0 PME-Enable- DSel=0 DScale=0 PME-

00: 02 10 52 47 87 00 90 02 27 00 00 03 10 40 00 00
10: 00 00 00 fd 01 b8 00 00 00 f0 4f fe 00 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 02 10 08 80
30: 00 00 4c fe 5c 00 00 00 00 00 00 0b 01 08 00
40: 0c 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 02 5c 10 00 01 00 00 ff 00 00 00 00 01 00 02 06
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

02:1c.0 PIC: Intel Corp. 82870P2 P64H2 I/OxAPIC (rev 04) (prog-if 20 [IO(X)-APIC])

Subsystem: Intel Corp. 82870P2 P64H2 I/OxAPIC

Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 0

Region 0: Memory at fe7fe000 (32-bit, non-prefetchable) [size=4K]

Capabilities: [50] PCI-X non-bridge device.

Command: DPERE- ERO- RBC=0 OST=0

Status: Bus=2 Dev=28 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0, DMOST=0, DMCRS=0, RSCEM=00: 86 80 61 14 06 01 30 00 04 20 00 08 00 00 00 00

10: 00 e0 7f fe 00 00 00 00 00 00 00 00 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 86 80 61 14
30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 00
40: 14 88 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 07 00 00 00 e0 02 03 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 3f 00 00 00 3f 00 00 00 3f 00 00 00 3f 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: f4 00 00 00 f4 00 00 00 f4 00 00 00 f4 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

02:1d.0 PCI bridge: Intel Corp. 82870P2 P64H2 Hub PCI Bridge (rev 04) (prog-if 00 [Normal decode])
Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+
FastB2B-

Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort-
>SERR- <PERR-

Latency: 64, cache line size 10

Bus: primary=02, secondary=04, subordinate=04, sec-latency=64

Memory behind bridge: fe600000-fe6ffff

Prefetchable memory behind bridge: 00000000ff500000-00000000ff500000

BridgeCtl: Parity- SERR+ NoISA+ VGA- MAbort- >Reset- FastB2B-

Capabilities: [50] PCI-X non-bridge device.

Command: DPERE+ ERO+ RBC=0 OST=0

Status: Bus=2 Dev=29 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0,
DMOST=0, DMCRS=0, RSCEM=00: 86 80 60 14 06 01 30 00 04 00 04 06 10 40 01 00

10: 00 00 00 00 00 00 00 00 02 04 04 40 f0 00 a0 22

20: 60 fe 60 fe 51 ff 51 ff 00 00 00 00 00 00 00 00

30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 06 00

40: 80 e1 08 00 00 00 00 00 00 00 00 00 00 00 00 00

50: 07 00 83 00 e8 02 03 00 ff ff ff ff ff ff ff ff

60: 00 04 e0 00 00 00 00 00 00 00 00 00 0f 0a 00 00

70: 00 f8 00 00 00 00 00 00 04 e8 02 00 00 00 00 00

80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

e0: 8f c3 00 00 22 20 00 00 00 00 00 00 00 00 00 00

f0: 10 02 00 00 00 00 00 00 32 22 37 77 ff bf ff bf

02:1e.0 PIC: Intel Corp. 82870P2 P64H2 I/OxAPIC (rev 04) (prog-if 20 [IO(X)-APIC])

Subsystem: Intel Corp. 82870P2 P64H2 I/OxAPIC

Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+
FastB2B-

Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort-
>SERR- <PERR-

Latency: 0

Region 0: Memory at fe7ff000 (32-bit, non-prefetchable) [size=4K]

Capabilities: [50] PCI-X non-bridge device.

Command: DPERE- ERO- RBC=0 OST=0

Status: Bus=2 Dev=30 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0,
DMOST=0, DMCRS=0, RSCEM=00: 86 80 61 14 06 01 30 00 04 20 00 08 00 00 00 00

10: 00 f0 7f fe 00 00 00 00 00 00 00 00 00 00 00 00

20: 00 00 00 00 00 00 00 00 00 00 00 00 86 80 61 14

30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 00 00

40: 10 88 00 00 00 00 00 00 00 00 00 00 00 00 00 00

50: 07 00 00 00 f0 02 03 00 00 00 00 00 00 00 00 00

60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

80: 3f 00 00 00 3f 00 00 00 3f 00 00 00 3f 00 00 00

90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

a0: 54 00 00 00 54 00 00 00 54 00 00 00 54 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

02:1f.0 PCI bridge: Intel Corp. 82870P2 P64H2 Hub PCI Bridge (rev 04) (prog-if 00 [Normal decode])
Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 64, cache line size 10

Bus: primary=02, secondary=03, subordinate=03, sec-latency=64

I/O behind bridge: 0000c000-0000dfff

Memory behind bridge: fe500000-fe5fffff

Prefetchable memory behind bridge: 00000000ff400000-00000000ff400000

BridgeCtl: Parity- SERR+ NoISA+ VGA- MAbort- >Reset- FastB2B-

Capabilities: [50] PCI-X non-bridge device.

Command: DPERE+ ERO+ RBC=0 OST=4

Status: Bus=2 Dev=31 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0, DMOST=0, DMCRS=0, RSCEM=00: 86 80 60 14 07 01 30 00 04 00 04 06 10 40 01 00

10: 00 00 00 00 00 00 00 00 02 03 03 40 c0 d0 a0 22

20: 50 fe 50 fe 41 ff 41 ff 00 00 00 00 00 00 00 00

30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 06 00

40: c0 f1 08 00 01 00 00 00 00 00 00 00 00 00 00 00

50: 07 00 c3 00 f8 02 03 00 ff ff ff ff ff ff ff ff

60: 00 04 f0 00 00 00 00 00 00 00 00 00 0f 0a 00 00

70: 00 f8 00 00 00 00 00 00 03 f8 02 00 00 00 00 00

80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

e0: 8f c3 00 00 22 20 00 00 00 00 00 00 00 00 00 00

f0: 32 03 04 c0 5b 21 00 00 32 22 37 77 ff bf ff bf

05:1c.0 PIC: Intel Corp. 82870P2 P64H2 I/OxAPIC (rev 04) (prog-if 20 [IO(X)-APIC])

Subsystem: Intel Corp. 82870P2 P64H2 I/OxAPIC

Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 0

Region 0: Memory at feafe000 (32-bit, non-prefetchable) [size=4K]

Capabilities: [50] PCI-X non-bridge device.

Command: DPERE- ERO- RBC=0 OST=0

Status: Bus=5 Dev=28 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0, DMOST=0, DMCRS=0, RSCEM=00: 86 80 61 14 06 01 30 00 04 20 00 08 00 00 00 00

10: 00 e0 af fe 00 00 00 00 00 00 00 00 00 00 00 00

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

20: 00 00 00 00 00 00 00 00 00 00 00 00 86 80 61 14
30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 00
40: 04 88 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 07 00 00 00 e0 05 03 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 11 00 00 00 11 00 00 00 11 00 00 00 11 00 00
90: 00 00 00 01 00 00 00 01 00 00 00 01 00 00 01
a0: 70 00 00 00 70 00 00 00 70 00 00 00 70 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

05:1d.0 PCI bridge: Intel Corp. 82870P2 P64H2 Hub PCI Bridge (rev 04) (prog-if 00 [Normal decode])
Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+
FastB2B-
Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort-
>SERR- <PERR-
Latency: 64, cache line size 10
Bus: primary=05, secondary=07, subordinate=07, sec-latency=64
I/O behind bridge: 0000e000-0000efff
Memory behind bridge: fe900000-fe9fffff
Prefetchable memory behind bridge: 00000000ff800000-00000000ff800000
BridgeCtl: Parity- SERR+ NoISA+ VGA- MAbort- >Reset- FastB2B-
Capabilities: [50] PCI-X non-bridge device.

Command: DPERE+ ERO+ RBC=0 OST=4
Status: Bus=5 Dev=29 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0,
DMOST=0, DMCRS=0, RSCEM=00: 86 80 60 14 07 01 30 00 04 00 04 06 10 40 01 00
10: 00 00 00 00 00 00 00 00 05 07 07 40 e0 e0 a0 22
20: 90 fe 90 fe 81 ff 81 ff 00 00 00 00 00 00 00 00
30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 06 00
40: c0 01 08 00 01 00 00 00 00 00 00 00 00 00 00
50: 07 00 c3 00 e8 05 03 00 ff ff ff ff ff ff ff
60: 00 04 e0 00 00 00 00 00 00 00 00 00 0f 0a 00 00
70: 00 f8 00 00 00 00 00 07 e8 05 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 8f c3 00 00 21 20 00 00 00 00 00 00 00 00 00
f0: 10 02 00 00 00 00 00 32 22 37 77 ff bf ff bf

05:1e.0 PIC: Intel Corp. 82870P2 P64H2 I/OxAPIC (rev 04) (prog-if 20 [IO(X)-APIC])
Subsystem: Intel Corp. 82870P2 P64H2 I/OxAPIC
Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+
FastB2B-
Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort-

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

>SERR- <PERR-

Latency: 0

Region 0: Memory at feaff000 (32-bit, non-prefetchable) [size=4K]

Capabilities: [50] PCI-X non-bridge device.

Command: DPERE- ERO- RBC=0 OST=0

Status: Bus=5 Dev=30 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0, DMOST=0, DMCRS=0, RSCEM=00: 86 80 61 14 06 01 30 00 04 20 00 08 00 00 00 00

10: 00 f0 af fe 00 00 00 00 00 00 00 00 00 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 00 86 80 61 14
30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 00 00
40: 00 88 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 07 00 00 00 f0 05 03 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 3f 00 00 00 3f 00 00 00 3f 00 00 00 3f 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 90 00 00 00 90 00 00 00 90 00 00 00 90 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
f0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

05:1f.0 PCI bridge: Intel Corp. 82870P2 P64H2 Hub PCI Bridge (rev 04) (prog-if 00 [Normal decode])

Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B-

Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR-

Latency: 64, cache line size 10

Bus: primary=05, secondary=06, subordinate=06, sec-latency=64

Memory behind bridge: fe800000-fe8ffff

Prefetchable memory behind bridge: 00000000ff700000-00000000ff700000

BridgeCtl: Parity- SERR+ NoISA+ VGA- MAbort- >Reset- FastB2B-

Capabilities: [50] PCI-X non-bridge device.

Command: DPERE+ ERO+ RBC=0 OST=0

Status: Bus=5 Dev=31 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=0, DMOST=0, DMCRS=0, RSCEM=00: 86 80 60 14 06 01 30 00 04 00 04 06 10 40 01 00

10: 00 00 00 00 00 00 00 00 05 06 06 40 f0 00 a0 22
20: 80 fe 80 fe 71 ff 71 ff 00 00 00 00 00 00 00 00
30: 00 00 00 00 50 00 00 00 00 00 00 00 00 00 06 00
40: 40 00 08 00 01 00 00 00 00 00 00 00 00 00 00 00
50: 07 00 03 00 f8 05 03 00 ff ff ff ff ff ff ff
60: 00 04 f0 00 00 00 00 00 00 00 00 00 00 0a 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
e0: af c3 00 00 23 20 00 00 00 00 00 00 00 00 00 00

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

f0: 32 03 04 c2 5b 21 00 00 32 22 37 77 ff bf ff bf

07:01.0 Ethernet controller: Intel Corp. 82546EB Gigabit Ethernet Controller (Copper) (rev 01)
Subsystem: Intel Corp. PRO/1000 MT Dual Port Server Adapter
Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV+ VGASnoop- ParErr- Stepping- SERR+ FastB2B-
Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=medium >TAbort- <TAbort- <MAbort- >SERR- <PERR-
Latency: 64 (63750ns min), cache line size 10
Interrupt: pin A routed to IRQ 48
Region 0: Memory at fe9c0000 (64-bit, non-prefetchable) [size=128K]
Region 4: I/O ports at e880 [size=64]
Capabilities: [dc] Power Management version 2
Flags: PMEClk- DSI+ D1- D2- AuxCurrent=0mA PME(D0+,D1-,D2-,D3hot+,D3cold-)
Status: D0 PME-Enable- DSel=0 DScale=1 PME-
Capabilities: [e4] PCI-X non-bridge device.
Command: DPERE- ERO+ RBC=0 OST=0
Status: Bus=7 Dev=1 Func=0 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=2, DMOST=0, DMCRS=1, RSCEM- Capabilities: [f0] Message Signalled Interrupts: 64bit+ Queue=0/0 Enable-

Address: 0000000000000000 Data: 0000
00: 86 80 10 10 17 01 30 02 01 00 00 02 10 40 80 00
10: 04 00 9c fe 00 00 00 00 00 00 00 00 00 00 00 00
20: 81 e8 00 00 00 00 00 00 00 00 00 00 86 80 11 10
30: 00 00 00 00 dc 00 00 00 00 00 00 00 0f 01 ff 00
40: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 01 e4 22 48
e0: 00 20 00 28 07 f0 02 00 08 07 43 04 00 00 00 00
f0: 05 00 80 00 00 00 00 00 00 00 00 00 00 00 00 00

07:01.1 Ethernet controller: Intel Corp. 82546EB Gigabit Ethernet Controller (Copper) (rev 01)
Subsystem: Intel Corp. PRO/1000 MT Dual Port Server Adapter
Control: I/O+ Mem+ BusMaster+ SpecCycle- MemWINV+ VGASnoop- ParErr- Stepping- SERR+ FastB2B-
Status: Cap+ 66Mhz+ UDF- FastB2B- ParErr- DEVSEL=medium >TAbort- <TAbort- <MAbort- >SERR- <PERR-
Latency: 64 (63750ns min), cache line size 10
Interrupt: pin B routed to IRQ 49
Region 0: Memory at fe9e0000 (64-bit, non-prefetchable) [size=128K]
Region 4: I/O ports at ec00 [size=64]
Capabilities: [dc] Power Management version 2
Flags: PMEClk- DSI+ D1- D2- AuxCurrent=0mA PME(D0+,D1-,D2-,D3hot+,D3cold-)
Status: D0 PME-Enable- DSel=0 DScale=1 PME-

Linux-Kernel: Tyan i7501 Pro (S2721-533) lock-up (e1000?)

Capabilities: [e4] PCI-X non-bridge device.

Command: DPERE- ERO+ RBC=0 OST=0

Status: Bus=7 Dev=1 Func=1 64bit+ 133MHz+ SCD- USC-, DC=simple, DMMRBC=2,
DMOST=0, DMCRS=1, RSCEM- Capabilities: [f0] Message Signalled Interrupts: 64bit+ Queue=0/0
Enable-

Address: 0000000000000000 Data: 0000

00: 86 80 10 10 17 01 30 02 01 00 00 02 10 40 80 00
10: 04 00 9e fe 00 00 00 00 00 00 00 00 00 00 00 00
20: 01 ec 00 00 00 00 00 00 00 00 00 00 86 80 11 10
30: 00 00 00 00 dc 00 00 00 00 00 00 00 0f 02 ff 00
40: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
a0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
b0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
c0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
d0: 00 00 00 00 00 00 00 00 00 00 00 00 01 e4 22 48
e0: 00 20 00 28 07 f0 02 00 09 07 43 04 00 00 00 00
f0: 05 00 80 00 00 00 00 00 00 00 00 00 00 00 00 00

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