

[PATCH] add PCI ROMs to sysfs

Source: <http://linux.derkeiler.com/Mailing-Lists/Kernel/2004-07/6286.html>

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Per a big thread earlier today, this patch adds a 'rom' file to sysfs in the same place where we currently have 'config', 'irq', etc.

Note that the ROM is **not** cached, so userspace will have to save the ROM when a device is first detected if the card doesn't support address cycles to ROM space at the same time as other memory accesses. (I'm still open to changing this if people feel strongly, but as it stands, the 'rom' file is only slightly more dangerous than the 'config' file.)

Jon, am I missing something or is it possible for us to cache the ROM in userspace when it receives the hotplug event? I saw your DRM code, and for the case of ROMs at a nonstandard address, we can fixup the address for `pci_dev->resource[PCI_ROM_RESOURCE]` in `pci-quirks`, can't we?

Thoughts? I've tried to add cleanup code, but I'm not sure how acceptable it is and I don't have any way of testing it.

Thanks,
Jesse

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- text/plain attachment: [pci-sysfs-rom-4.patch](#)