

Re: [PATCH 0/9] x86-64 put current in r10

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From: Jari Ruusu (jariruusu_at_users.sourceforge.net)

Date: 11/30/05

Date: Wed, 30 Nov 2005 08:39:01 +0200

To: Benjamin LaHaise <bcrl@kvack.org>, Andi Kleen <ak@suse.de>

Benjamin LaHaise wrote:

> *The following emails contain the patches to convert x86-64 to store current
> in r10 (also at <http://www.kvack.org/~bcrl/patches/v2.6.15-rc3/>).*

[snip]

> *No benchmarks that I am aware of show regressions with this change.*

Ben,

Your patch breaks all out-of-tree amd64 assembler code used in kernel. r10 register is one of those registers that does not need to be preserved across function calls, and reserving that register for other purpose means that all assembler code using r10 in kernel must be rewritten. This is deeply unfunny.

Andi,

Please don't apply Ben's patch. It is already bad enough having to deal with two incompatible calling conventions on 32 bit x86.

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Jari Ruusu 1024R/3A220F51 5B 4B F9 BB D3 3F 52 E9 DB 1D EB E3 24 0E A9 DD

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