

Re: [PATCH 0/9] x86-64 put current in r10

Source: <http://linux.derkeiler.com/Mailing-Lists/Kernel/2005-12/msg00044.html>

- *From:* "Randy.Dunlap" <rdunlap@xxxxxxxxxxxxx>
 - *Date:* Wed, 30 Nov 2005 08:22:48 -0800 (PST)
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On Wed, 30 Nov 2005, Jari Ruusu wrote:

> Benjamin LaHaise wrote:
>> The following emails contain the patches to convert x86-64 to store current
>> in r10 (also at <http://www.kvack.org/~bcr1/patches/v2.6.15-rc3/>).
> [snip]
>> No benchmarks that I am aware of show regressions with this change.
>
> Ben,
> Your patch breaks all out-of-tree amd64 assembler code used in kernel. r10
> register is one of those registers that does not need to be preserved across
> function calls, and reserving that register for other purpose means that all
> assembler code using r10 in kernel must be rewritten. This is deeply
> unfunny.
>
> Andi,
> Please don't apply Ben's patch. It is already bad enough having to deal with
> two incompatible calling conventions on 32 bit x86.

Just for the sake of understanding the current kernel release process, when would something like this be acceptable/possible? Would it require a Linux 3.0 version, or at least a 2.8?

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~Randy

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- *Follow-Ups:*
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 ◇ *From:* Helge Hafting
 - ◆ **[Re: \[PATCH 0/9\] x86-64 put current in r10](#)**
 ◇ *From:* Kyle Moffett

Re: [PATCH 0/9] x86-64 put current in r10

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