

patch to make 2.4.32 work on i486 again

Source: <http://linux.derkeiler.com/Mailing-Lists/Kernel/2006-01/msg11571.html>

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 - *Date:* Tue, 31 Jan 2006 23:29:05 +0100 (CET)
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Booting the 2.4.32 kernel compiled for a i486 on an i486 box fails, because "Kernel compiled for Pentium+, requires TSC feature!" (printed from `check_config()` `include/asm-i386/bugs.h`). To reproduce, select 486 in the kernel configuration and `grep CONFIG_X86_TSC .config`

Seems strange that no one noticed this, am i the only one still using 486 boxes? :)

Jacek

Simple patch against vanilla 2.4.32:

```
--- arch/i386/config.in.old 2006-01-30 22:57:21.000000000 +0100
+++ arch/i386/config.in 2006-01-30 23:00:55.000000000 +0100
@@ -65,6 +65,7 @@
define_bool CONFIG_X86_POPAD_OK y
define_bool CONFIG_RWSEM_GENERIC_SPINLOCK n
define_bool CONFIG_RWSEM_XCHGADD_ALGORITHM y
+ define_bool CONFIG_X86_TSC n
fi
if [ "$CONFIG_M486" = "y" ]; then
define_int CONFIG_X86_L1_CACHE_SHIFT 4
@@ -72,6 +73,7 @@
define_bool CONFIG_X86_ALIGNMENT_16 y
define_bool CONFIG_X86_PPRO_FENCE y
define_bool CONFIG_X86_F00F_WORKS_OK n
+ define_bool CONFIG_X86_TSC n
fi
if [ "$CONFIG_M586" = "y" ]; then
define_int CONFIG_X86_L1_CACHE_SHIFT 5
-
```

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- *Follow-Ups:*

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◇ From: Grant Coady

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cpufreq-ppc-frequency-change-issues-freq-already-lowered-by-bios.patch added to -mm tree
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