

# [PATCH 19/40] KVM: allow access to EFER in 32bit KVM

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*Source:* <http://linux.derkeiler.com/Mailing-Lists/Kernel/2008-03/msg12005.html>

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This patch makes the EFER register accessible on a 32bit KVM host. This is necessary to boot 32 bit PAE guests under SVM.

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arch/x86/kvm/x86.c | 10 -----

1 files changed, 0 insertions(+), 10 deletions(-)

diff --git a/arch/x86/kvm/x86.c b/arch/x86/kvm/x86.c

index db16f23..38edb2f 100644

--- a/arch/x86/kvm/x86.c

+++ b/arch/x86/kvm/x86.c

```
@@ -432,8 +432,6 @@ static u32 emulated_msrs[] = {
MSR_IA32_MISC_ENABLE,
};
```

```
‑#ifdef CONFIG_X86_64
```

```
‑
```

```
static void set_efer(struct kvm_vcpu *vcpu, u64 efer)
```

```
{
```

```
if (efer & efer_reserved_bits) {
```

```
@@ -458,8 +456,6 @@ static void set_efer(struct kvm_vcpu *vcpu, u64 efer)
```

```
vcpu->arch.shadow_efer = efer;
```

```
}
```

```
‑#endif
```

```
‑
```

```
void kvm_enable_efer_bits(u64 mask)
```

```
{
```

```
efer_reserved_bits &= ~mask;
```

```
@@ -489,11 +485,9 @@ static int do_set_msr(struct kvm_vcpu *vcpu, unsigned index, u64 *data)
```

```
int kvm_set_msr_common(struct kvm_vcpu *vcpu, u32 msr, u64 data)
```

```
{
```

```
switch (msr) {
```

```
‑#ifdef CONFIG_X86_64
```

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```
case MSR_EFER:
set_efer(vcpu, data);
break;
--endif
case MSR_IA32_MC0_STATUS:
pr_unimpl(vcpu, "%s: MSR_IA32_MC0_STATUS 0x%llx, nop\n",
__FUNCTION__, data);
@@ -571,11 +565,9 @@ int kvm_get_msr_common(struct kvm_vcpu *vcpu, u32 msr, u64 *pdata)
case MSR_IA32_MISC_ENABLE:
data = vcpu->arch.ia32_misc_enable_msr;
break;
--ifdef CONFIG_X86_64
case MSR_EFER:
data = vcpu->arch.shadow_efer;
break;
--endif
default:
pr_unimpl(vcpu, "unhandled rdmsr: 0x%x\n", msr);
return 1;
@@ -2880,9 +2872,7 @@ int kvm_arch_vcpu_ioctl_set_sregs(struct kvm_vcpu *vcpu,
set_cr8(vcpu, sregs->cr8);

mmu_reset_needed |= vcpu->arch.shadow_efer != sregs->efer;
--ifdef CONFIG_X86_64
kvm_x86_ops->set_efer(vcpu, sregs->efer);
--endif
kvm_set_apic_base(vcpu, sregs->apic_base);

kvm_x86_ops->decache_cr4_guest_bits(vcpu);
--
1.5.4.5
```

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