

Re: What is Trigger Level in UART?

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- *From:* Richard <rkm@xxxxxxxxxxxx>
 - *Date:* Thu, 17 May 2007 21:07:46 -0400
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Stephen SM WONG wrote:

On Thu, 17 May 2007, google-rambo88 wrote:

Hi,
What is Trigger Level in UART?
I heard that We can commonly adjust it as 1, 4, 8 with setserial
command..

There are some buffers for receive / transmission in a UART. Say, in a plain 16550A, there are 16 bytes FIFO buffer for transmission, and another 16 bytes receive buffer.

One can program a UART to trigger an interrupt signal when a certain number of bytes are received (and occupied the receive buffer), or to trigger an interrupt when there are a few bytes free in the transmission buffer.

The design of the TX/RX buffers is to reduce no. of interrupt signals to the host system, so that, overhead of interrupt service routine can be reduced.

In some (more advanced) UART, there are larger RX/TX buffers, say 1kB x 2 on chip.

My 2 cents.

Stephen Wong @ Hong Kong

Does anyone know how to program the uart to send or receive a different number of bytes before generating an interrupt? I've tried using setserial but it doesn't seem to do anything.

Richard

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